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S/N 10/726,302

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	KURODA	Examiner:	J. Zweizig
Serial No.:	10/726,302	Group Art Unit:	2816
Filed:	December 1, 2003	Docket No.:	10873.1353US01
Title:	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE		

CERTIFICATE UNDER 37 CFR 1.6(d): I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on June 21, 2005.

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Respectfully submitted,

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